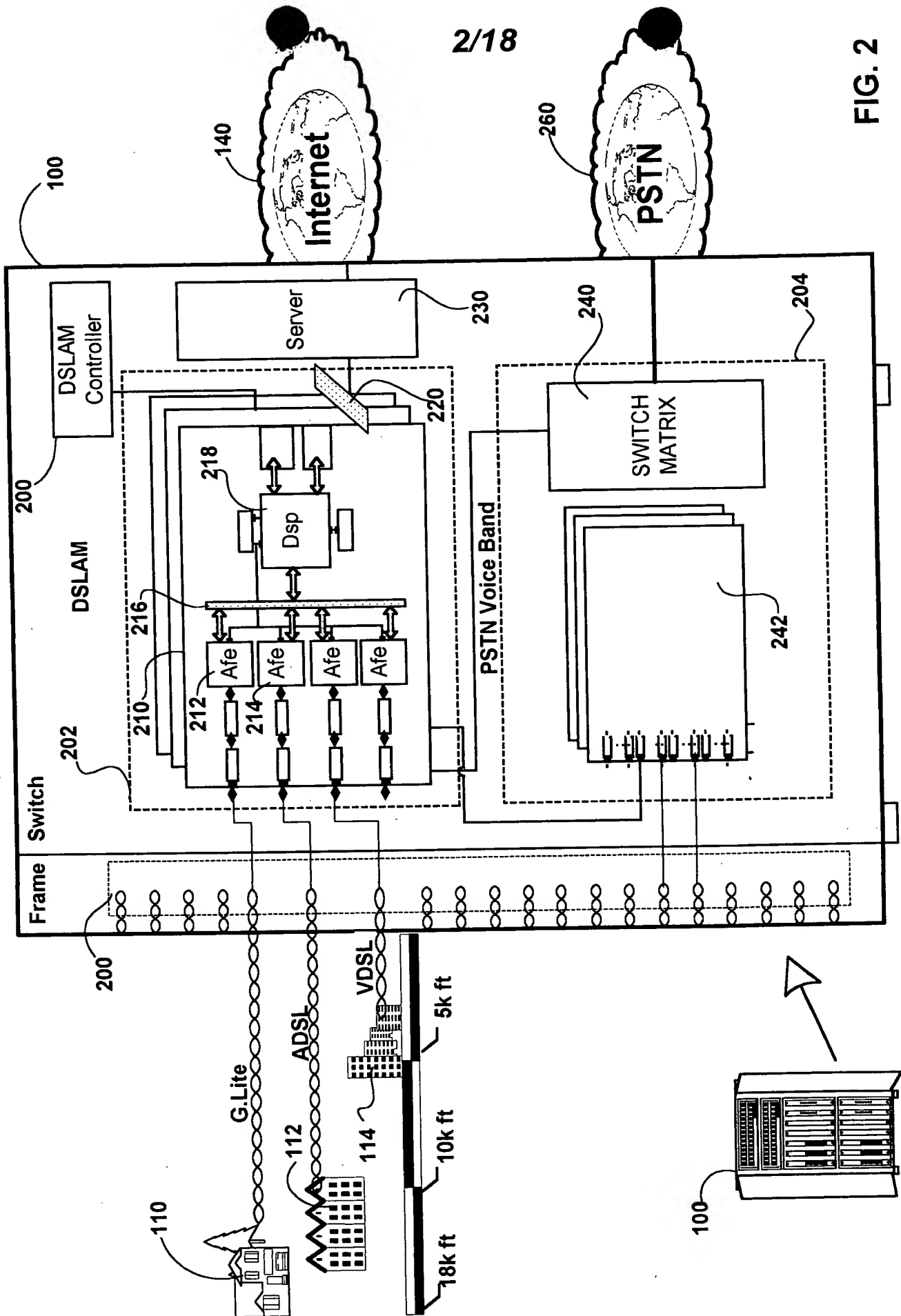


FIG. 1

Packet Based distributed xDSL System

FIG. 2



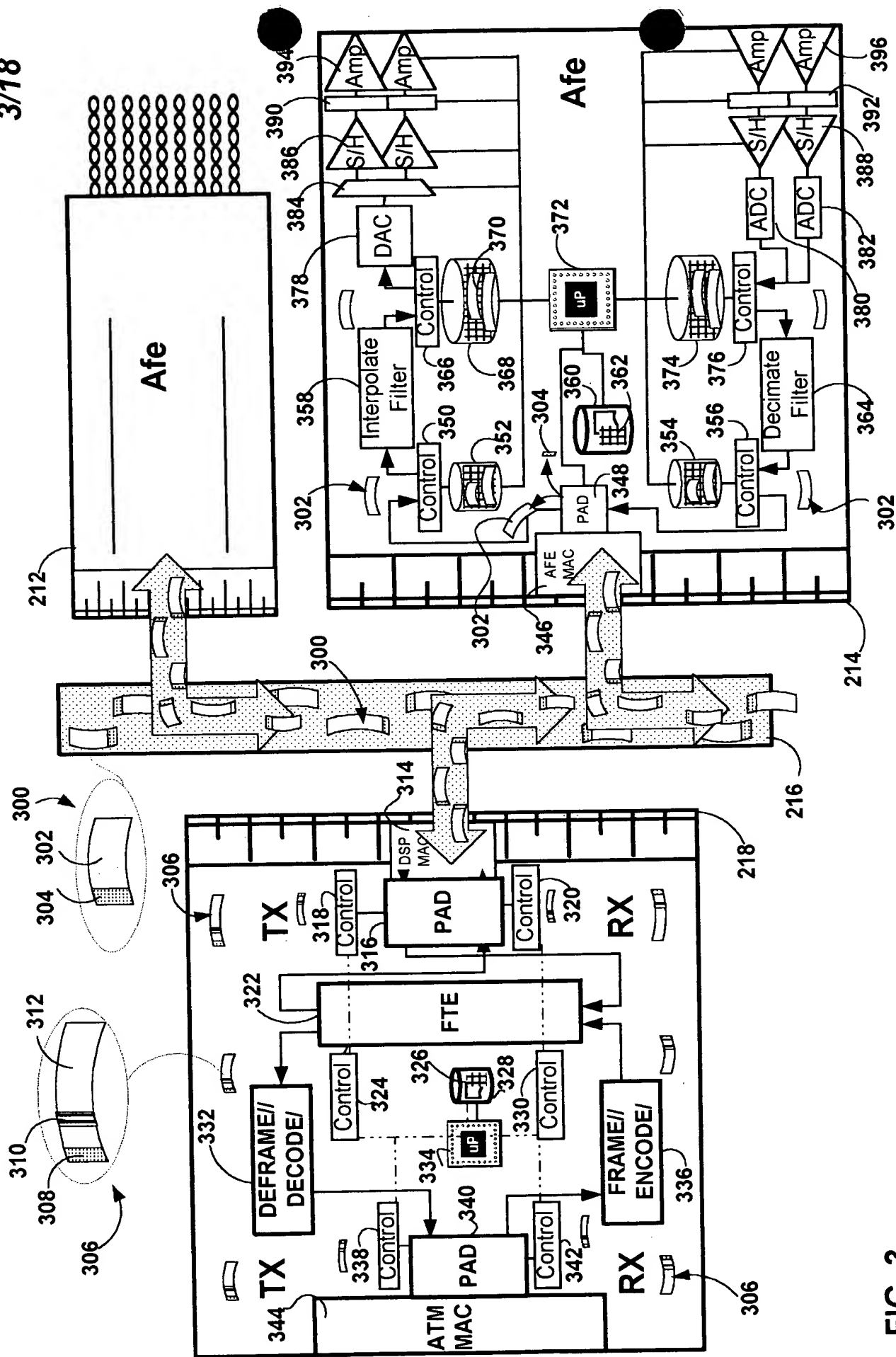


FIG. 3

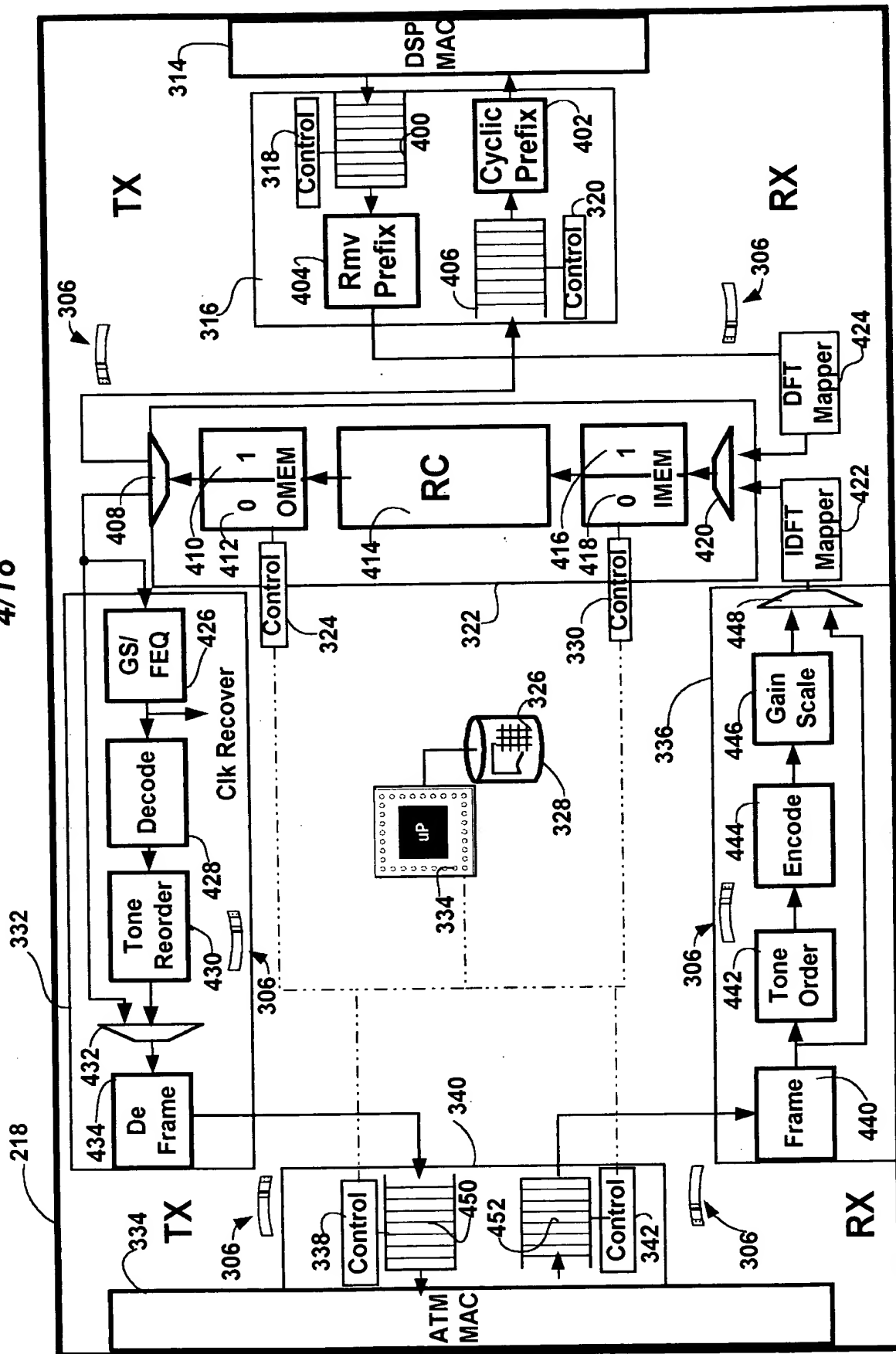
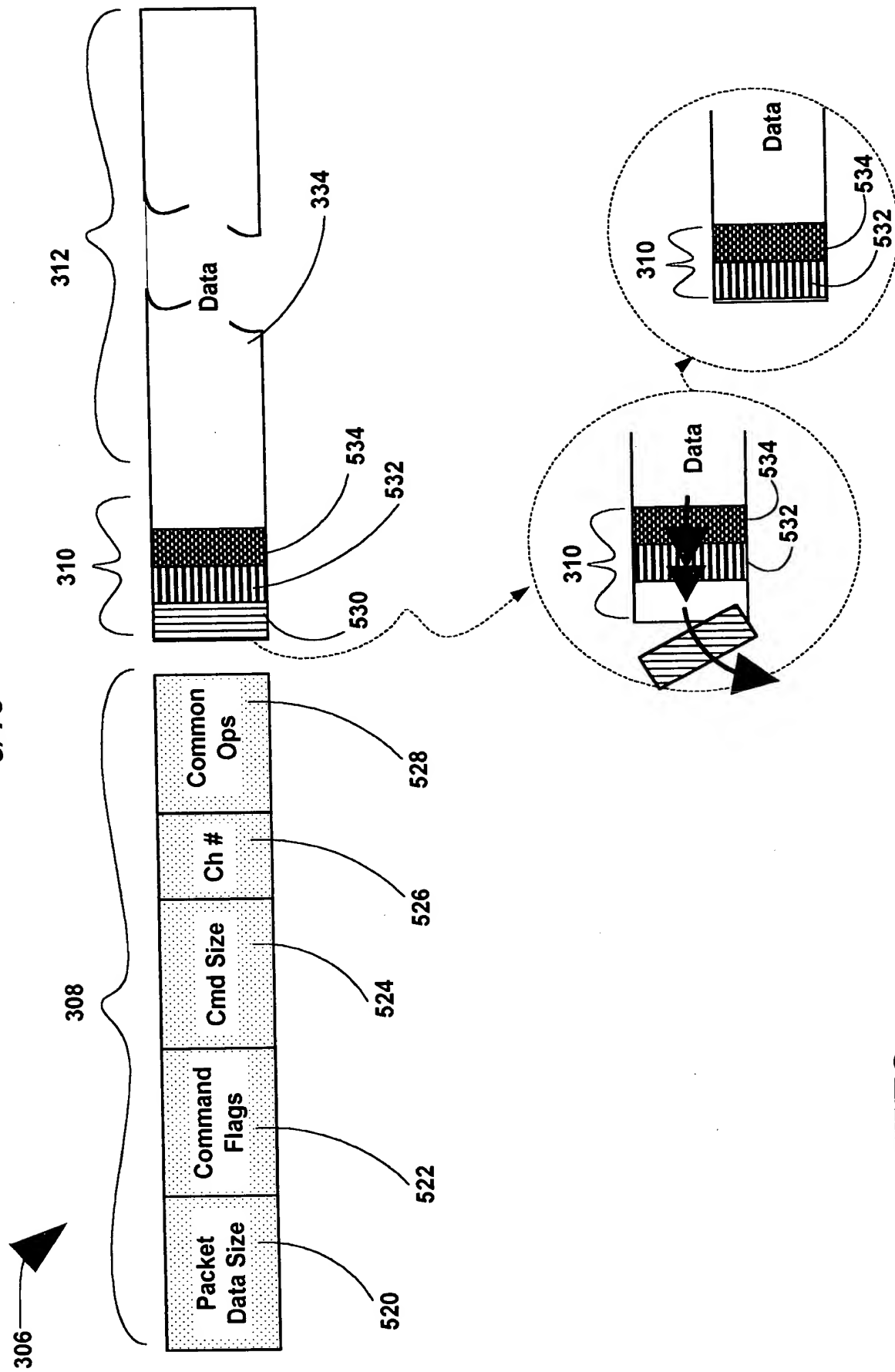


FIG. 4

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DSP PACKETS

FIG. 5

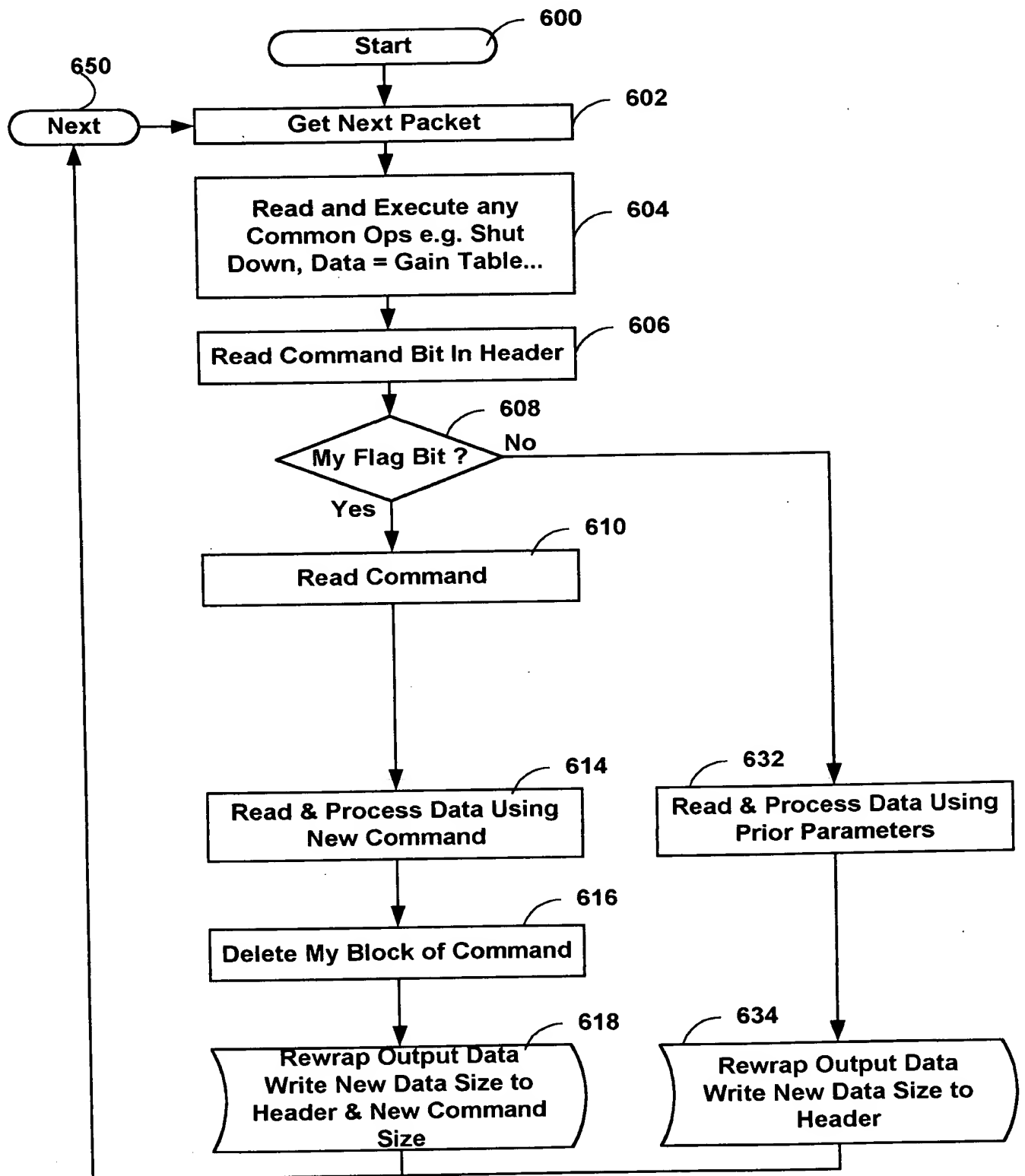
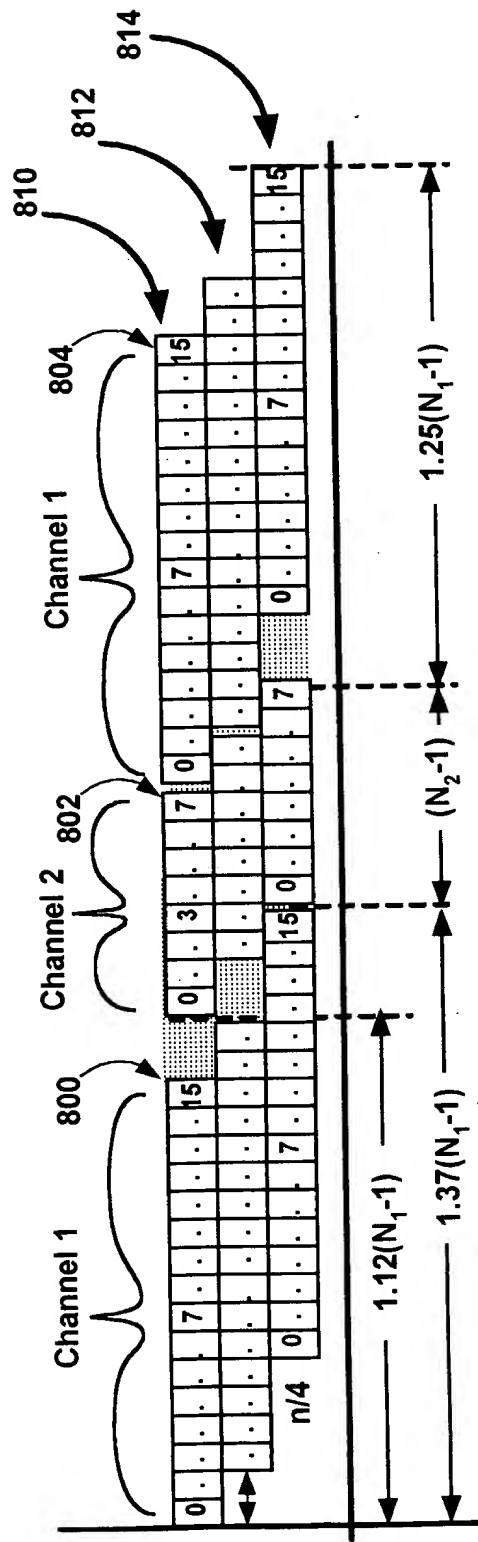
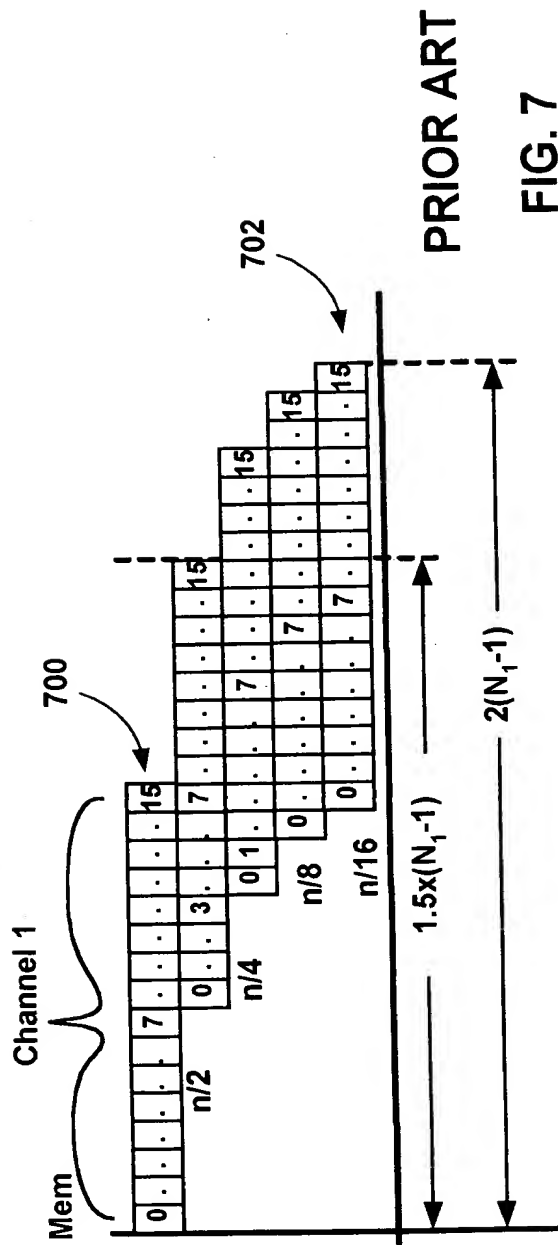


FIG. 6



009207-12886960

FIG. 9A

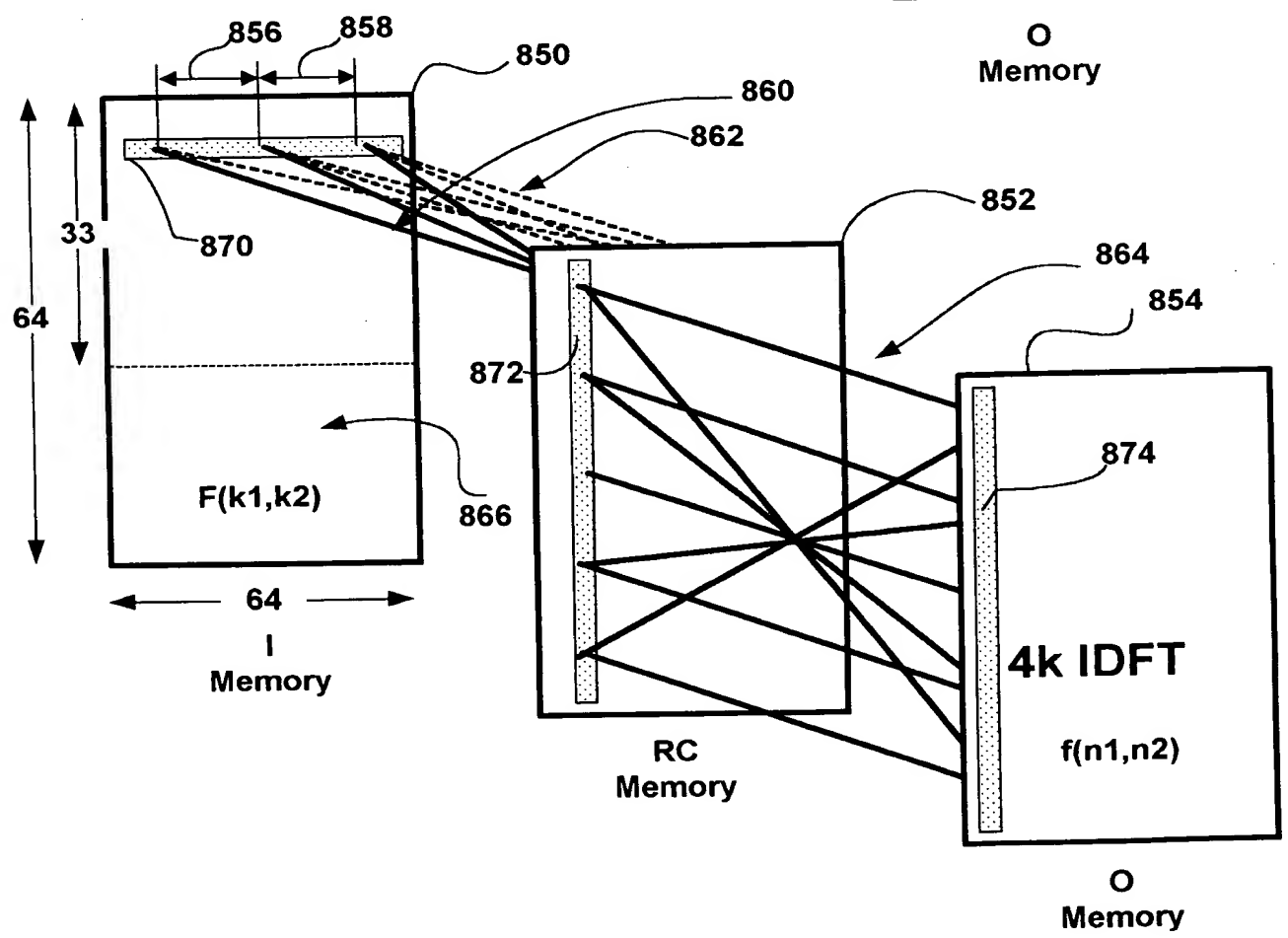
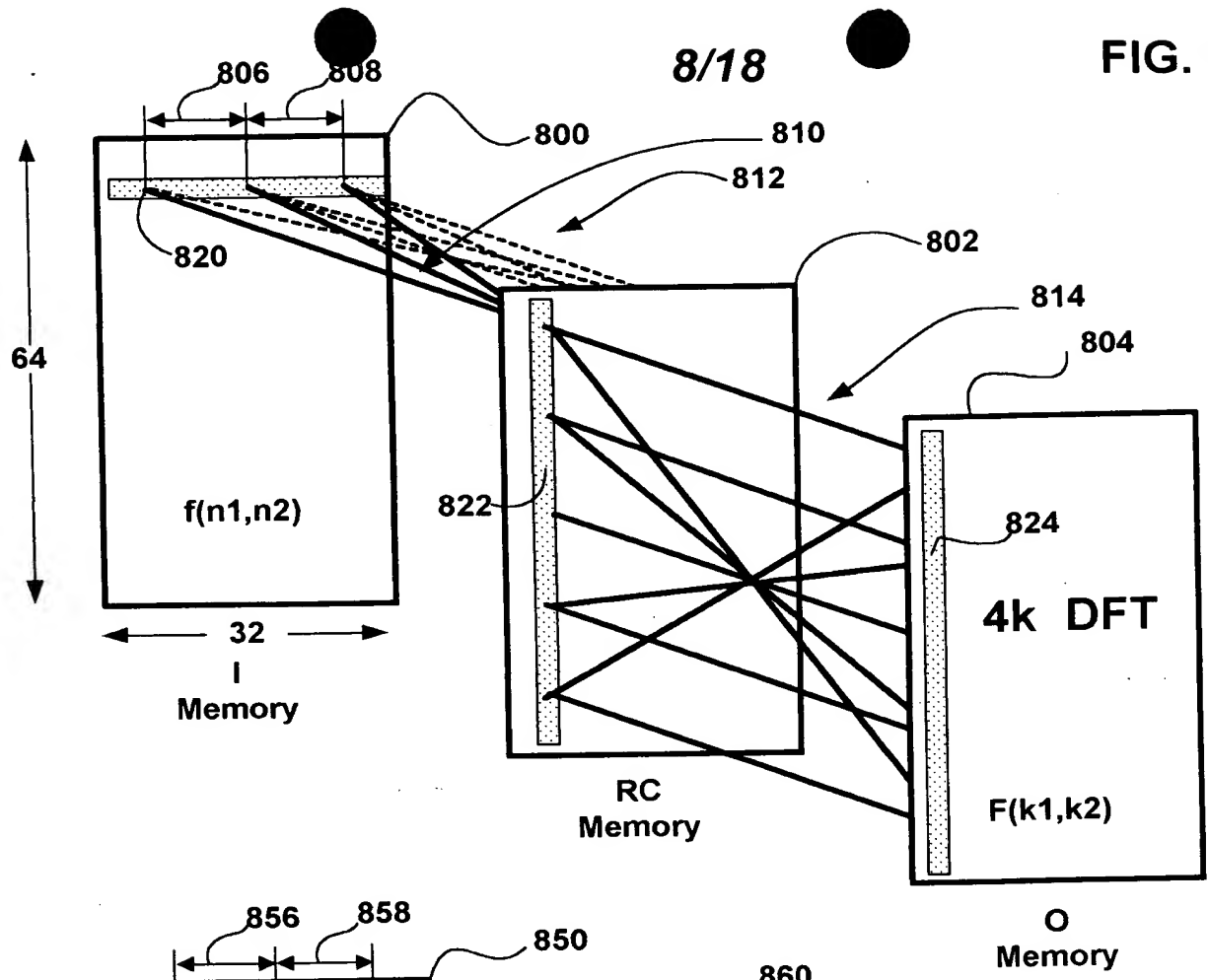


FIG. 9B

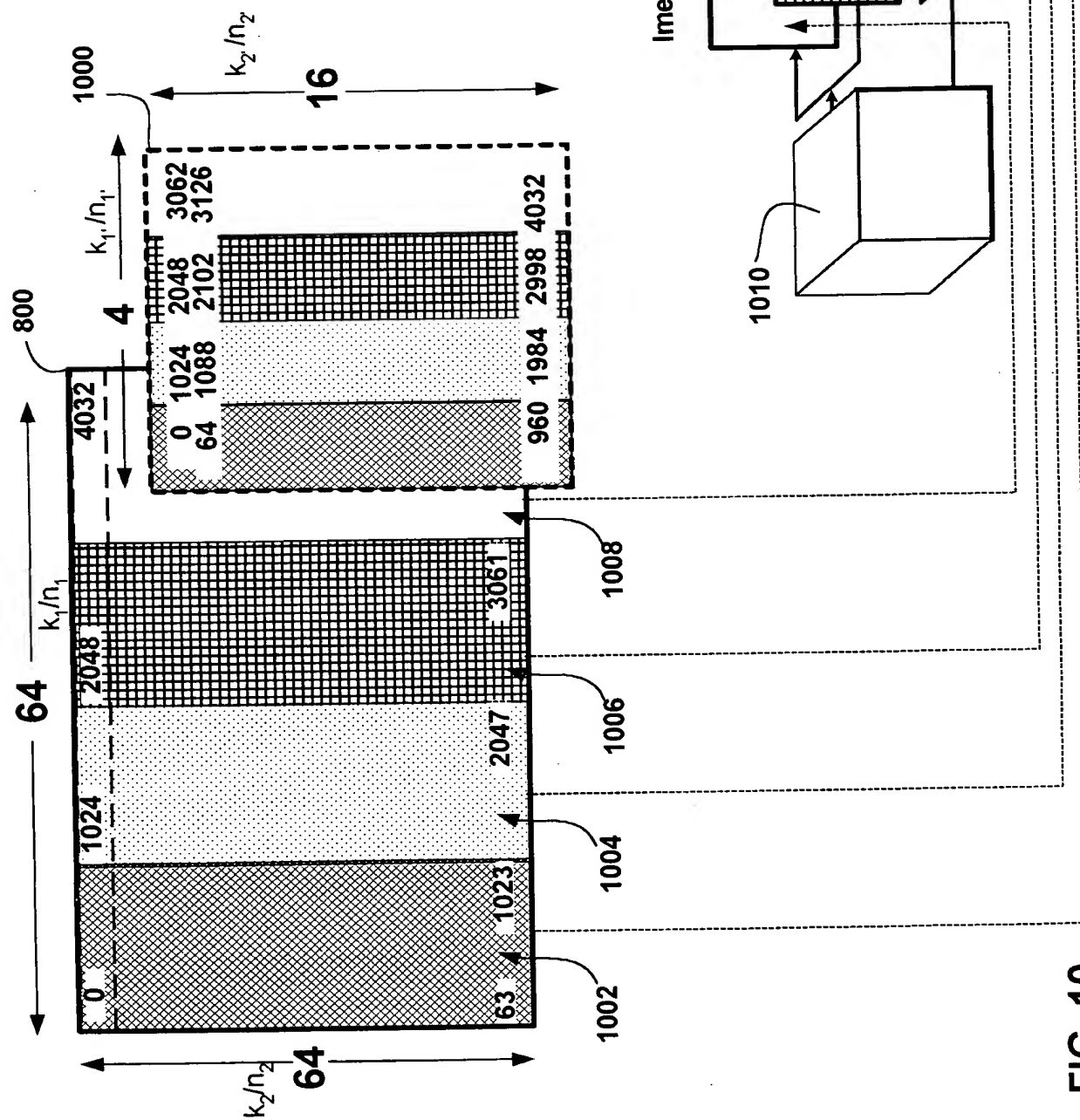


FIG. 11A

TRANSMIT		G.LITE	ADSL	VDSL	Other
Sample space		256	512	512 , 1024, 2048, 4096	
# Tones		128	256	256, 512, 1024, 2048	
Cyclic Prefix		16	32	Programmable	
		● ●	● ●	● ●	● ●
Switching		High Pass Filter	High Pass Filter	None	

FIG. 11C

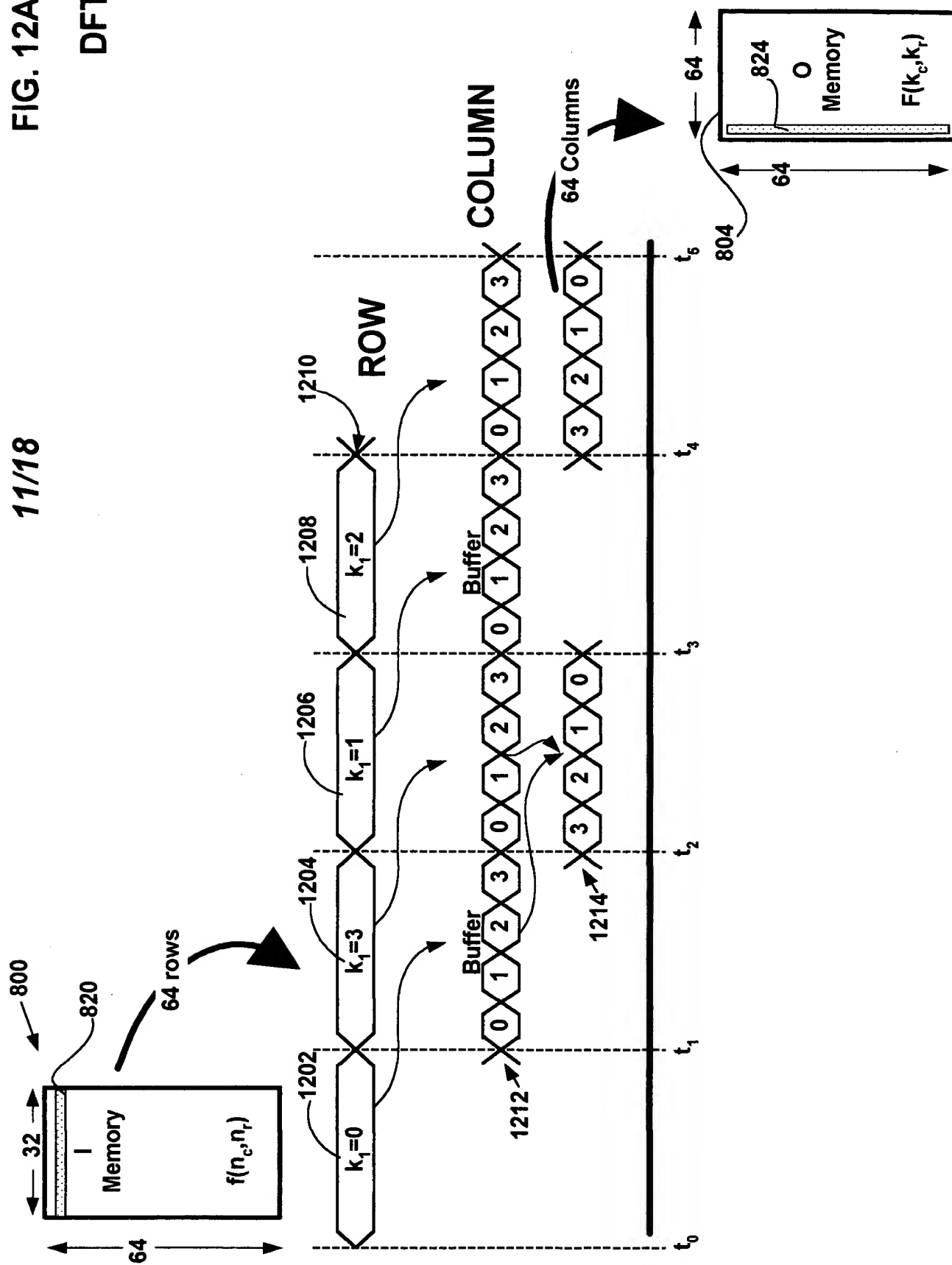
Sample	ROW TRANSFORM		COLUMN TRANSFORM
4k	64	16	64 16
2k	32	8	64 16
1k	32	8	32 8
512	16	4	32 8
256	16	4	16 4
128	8	2	16 4
64	64	16	1 1

FIG. 11B

RECEIVE		G.LITE	ADSL	VDSL	Other
Sample space		64	64	512 , 1024, 2048, 4096	
# Tones		32	32	256, 512, 1024, 2048	
Cyclic Prefix		4	4	Programmable	
		● ●	● ●	● ●	● ●
Switching					

FIG. 12A

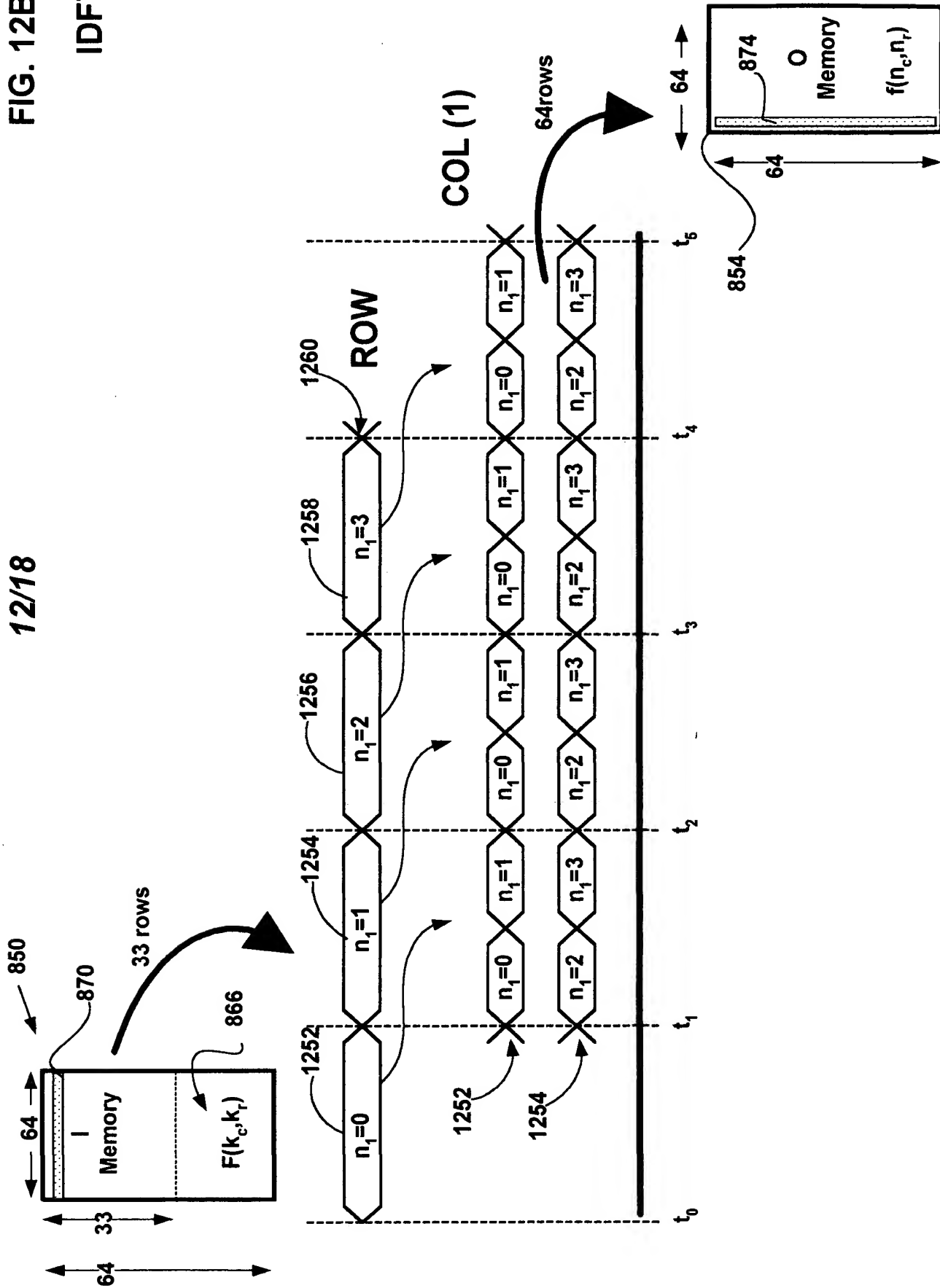
DET



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FIG. 12B

IDFT



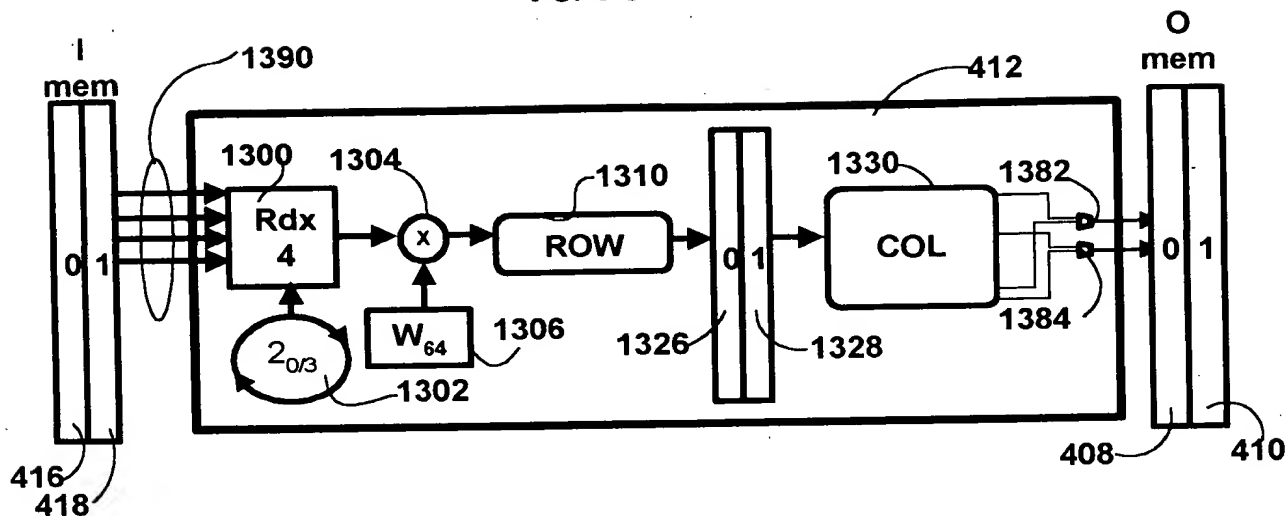


FIG. 13A

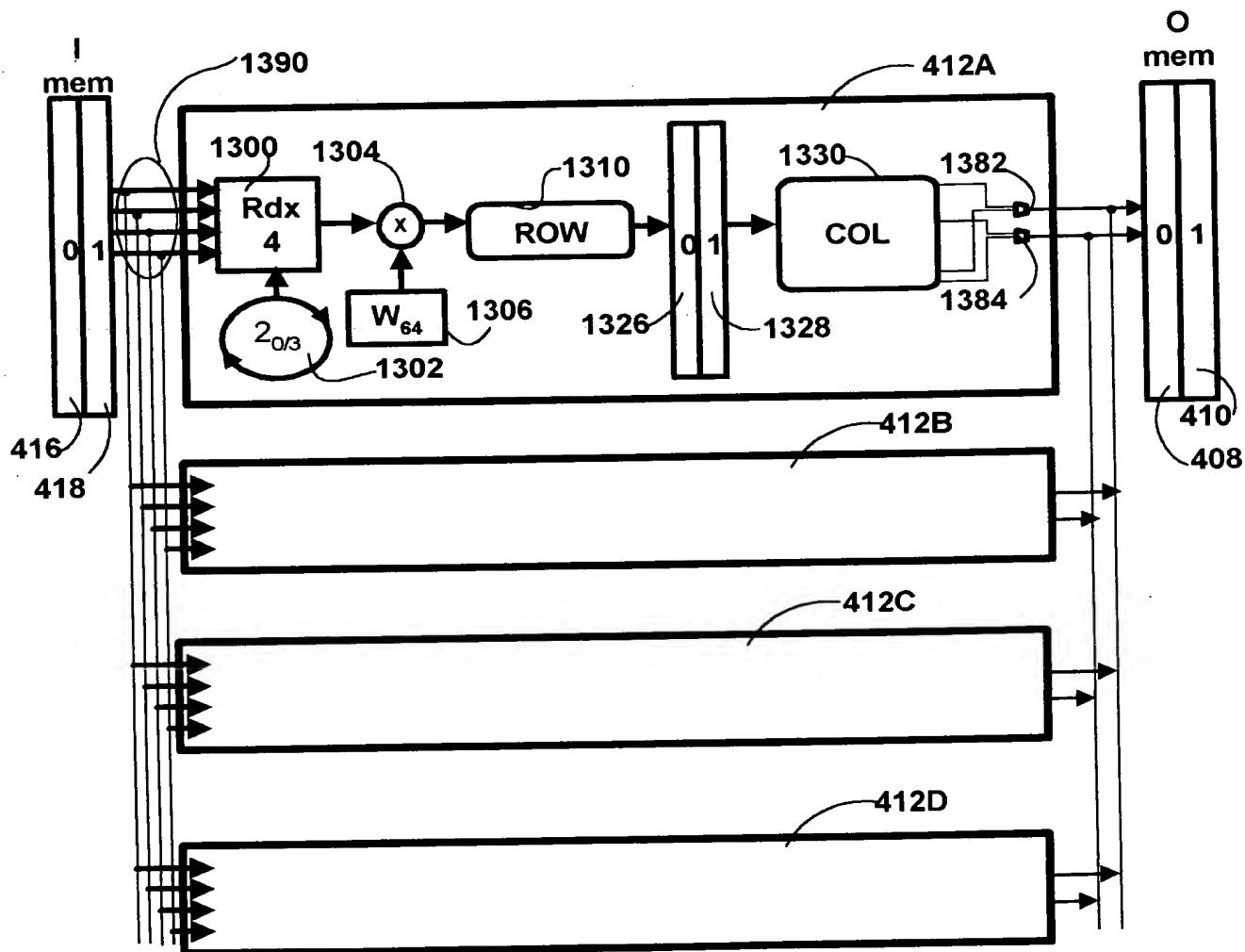


FIG. 13B

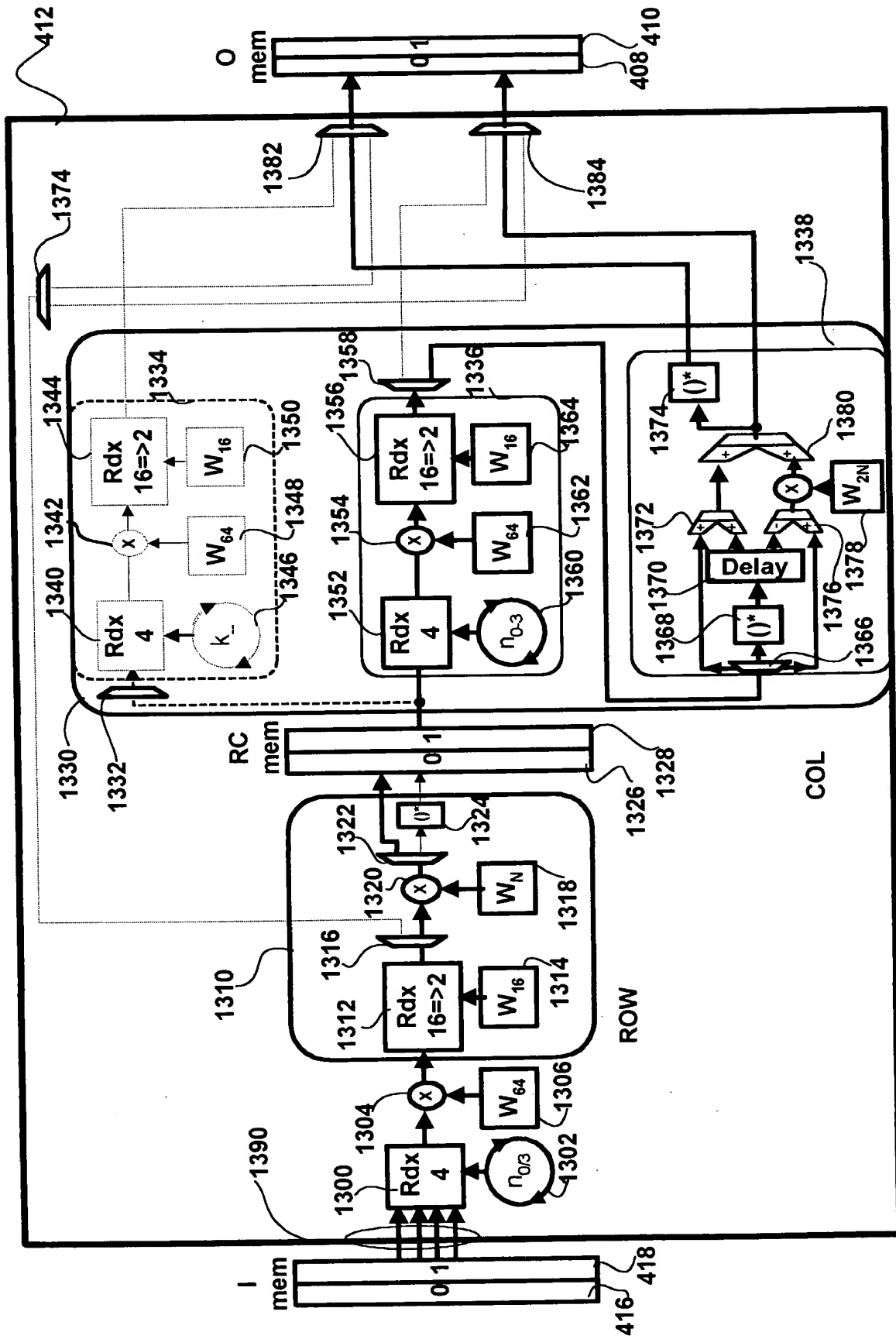


FIG. 13C

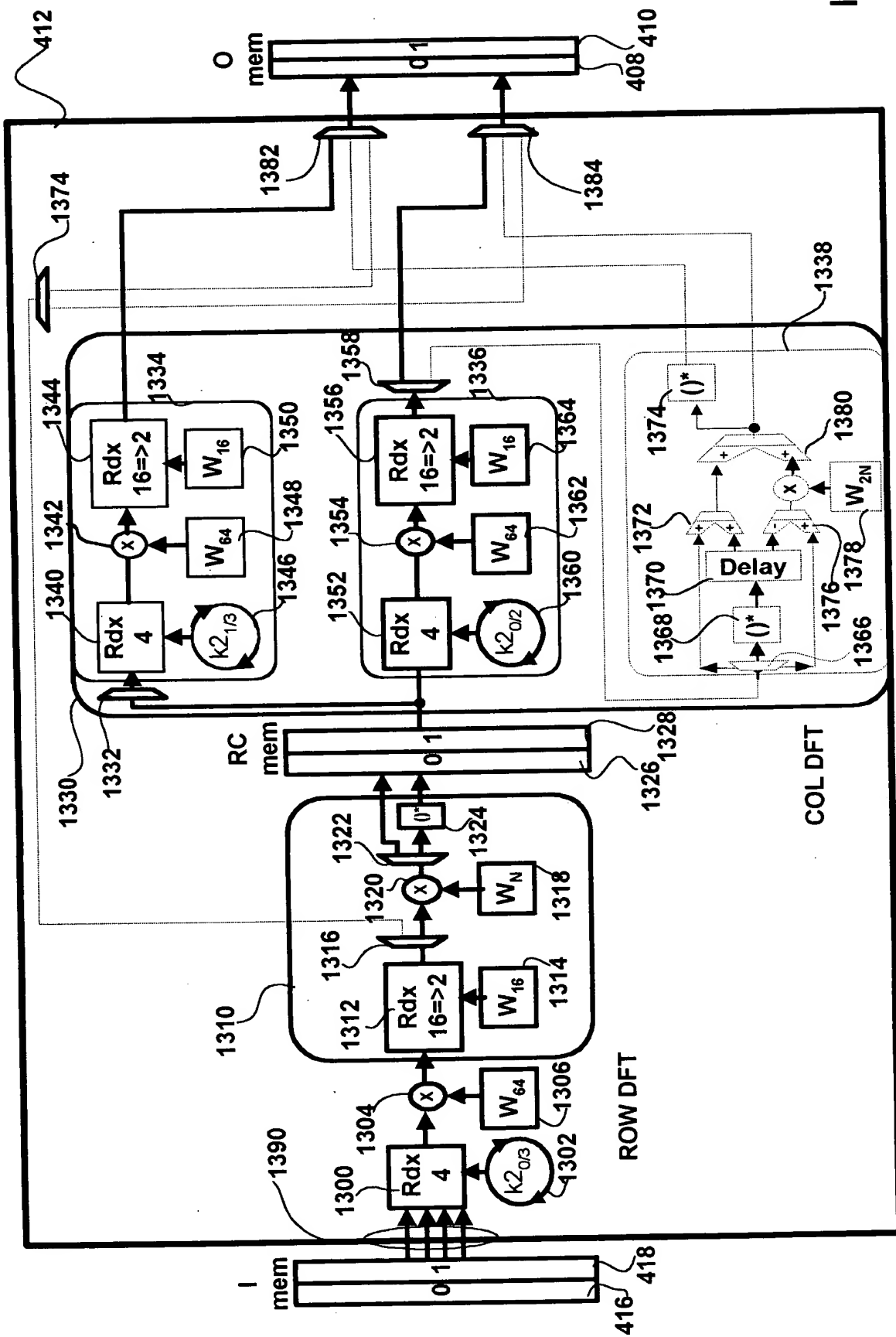


FIG. 13D

IDFT

COL DFT

ROW DFT

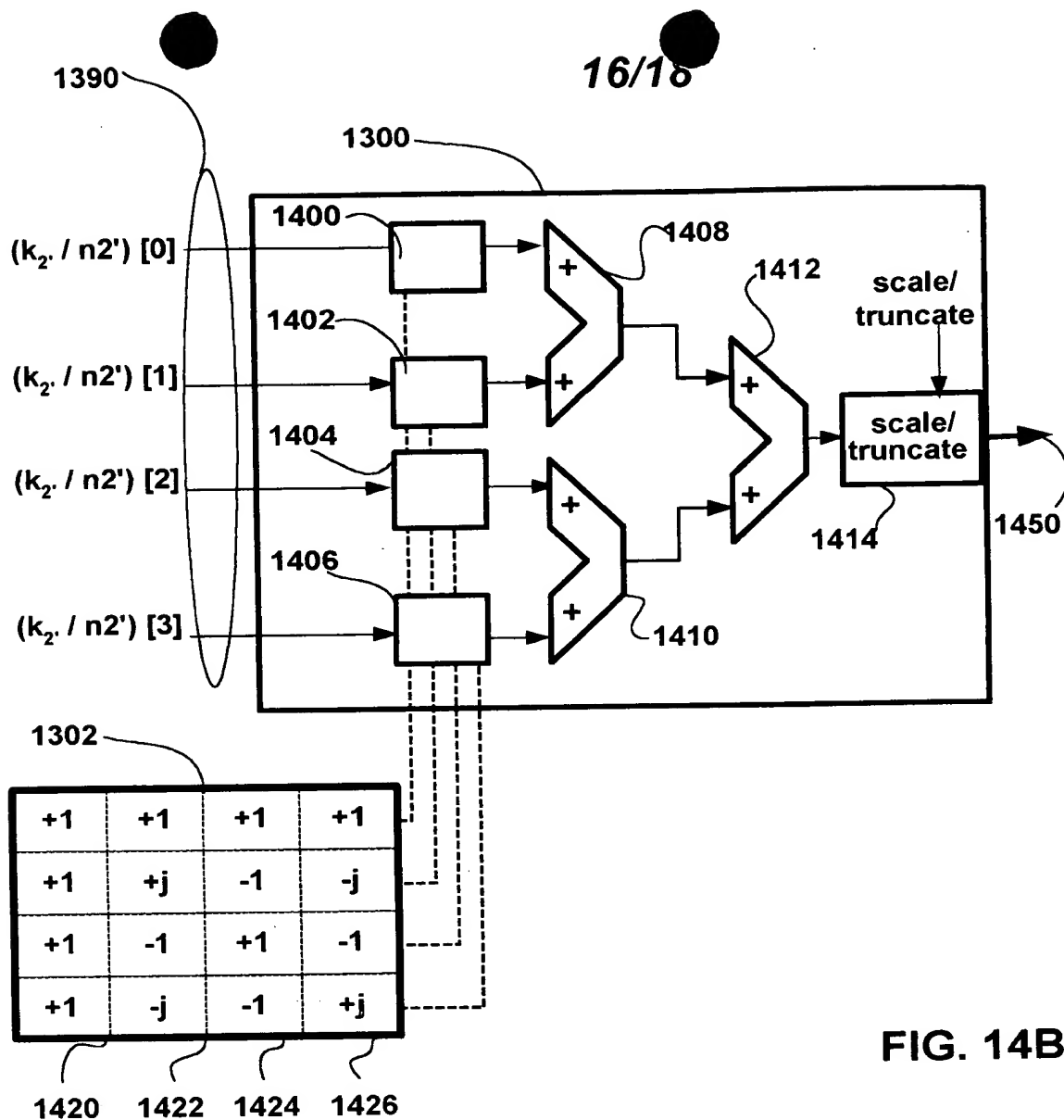


FIG. 14B

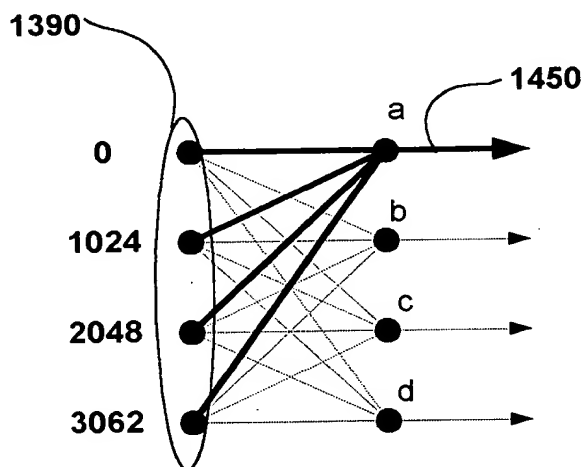


FIG. 14A

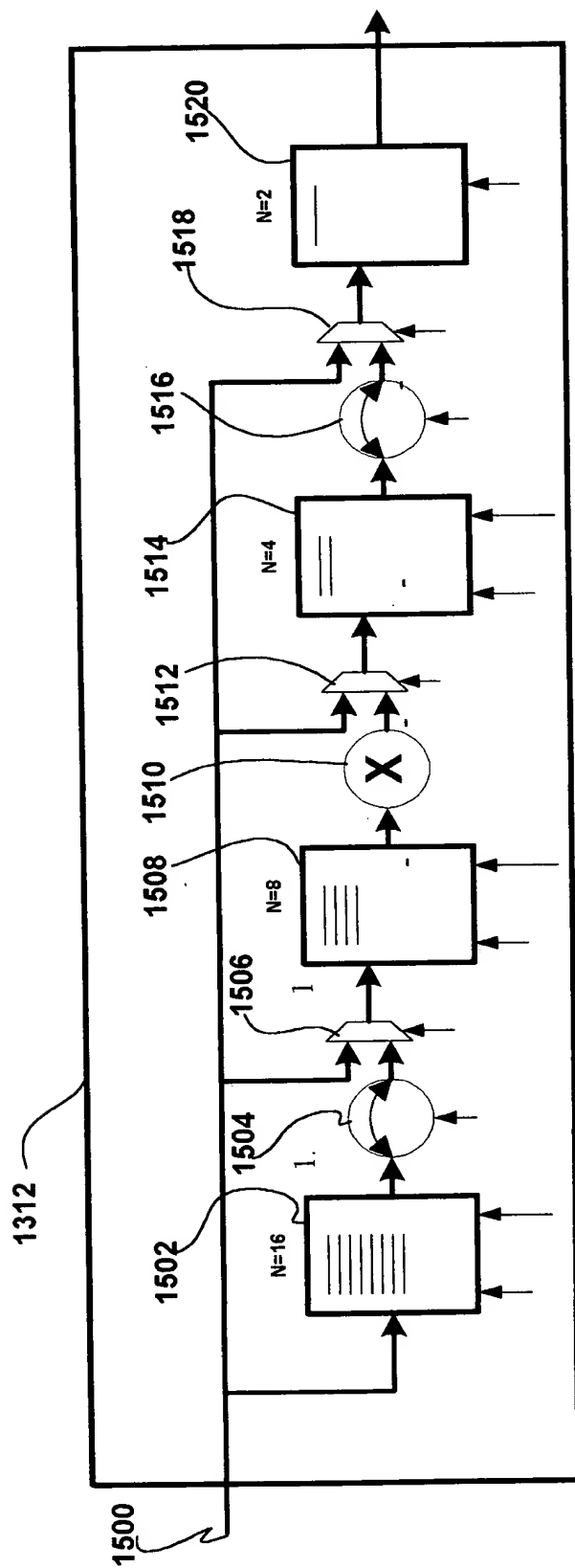


FIG. 15

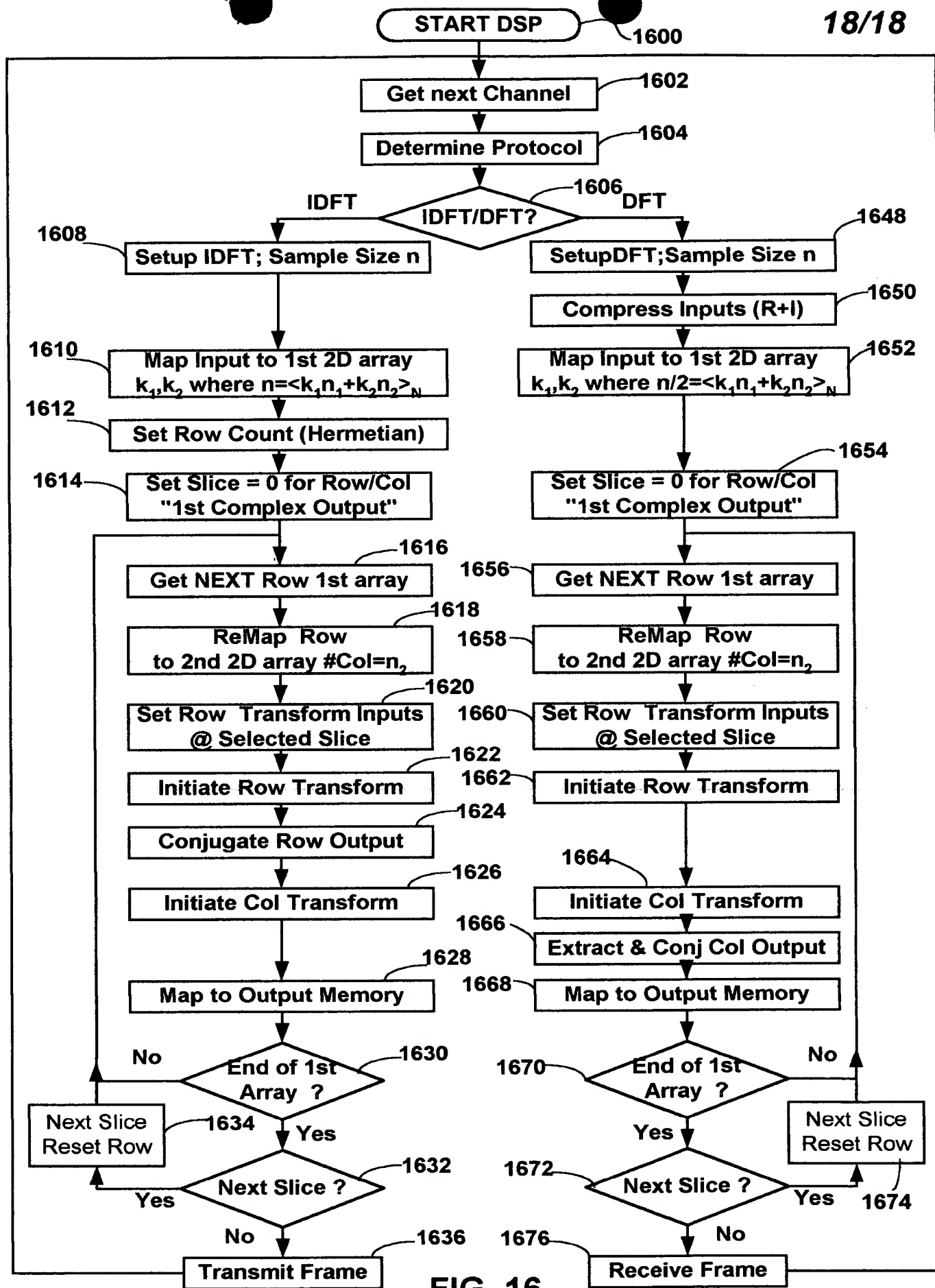


FIG. 16